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Bib Data Sheet

CONFIRMATION NO. 2540

SERIAL NUMBER 09/926,241	FILING DATE 09/28/2001 RULE	CLASS 370	GROUP ART UNIT 2661 2662	ATTORNEY DOCKET NO. P21482
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## APPLICANTS

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YES HN

## \*\* CONTINUING DATA \*\*\*\*\*

THIS APPLICATION IS A 371 OF PCT/JP01/00694 02/01/2001

YES HN

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN JP2000-26306 02/03/2000

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 6
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature Initials				

## ADDRESS

7055

## TITLE

Memory circuit and coherent detection circuit

FILING FEE RECEIVED 1100	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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